FIG 1

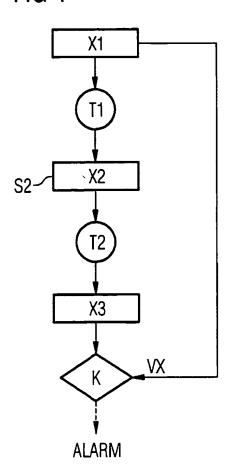


FIG 2

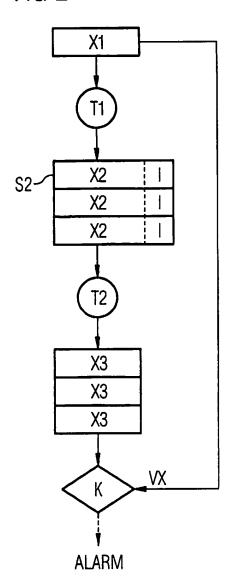


FIG 3

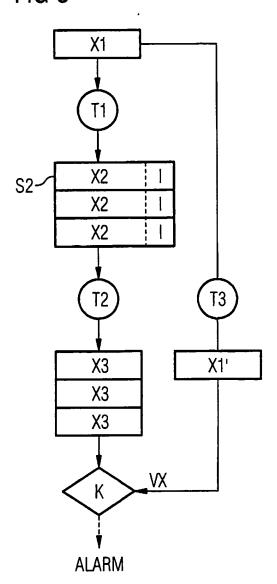


FIG 4

MEM

DEC

3

R1

CPU

ALARM

